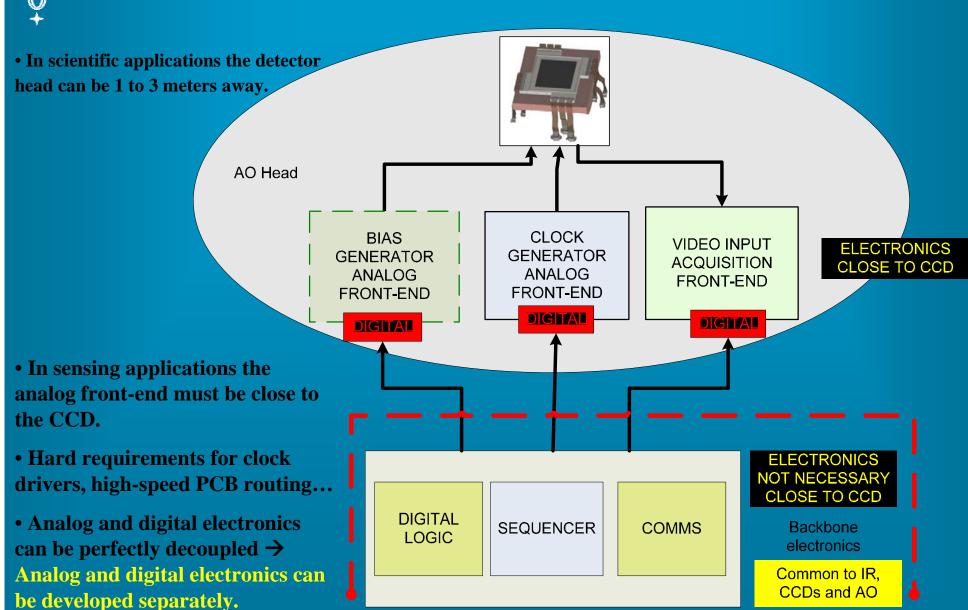
+ES+ O +

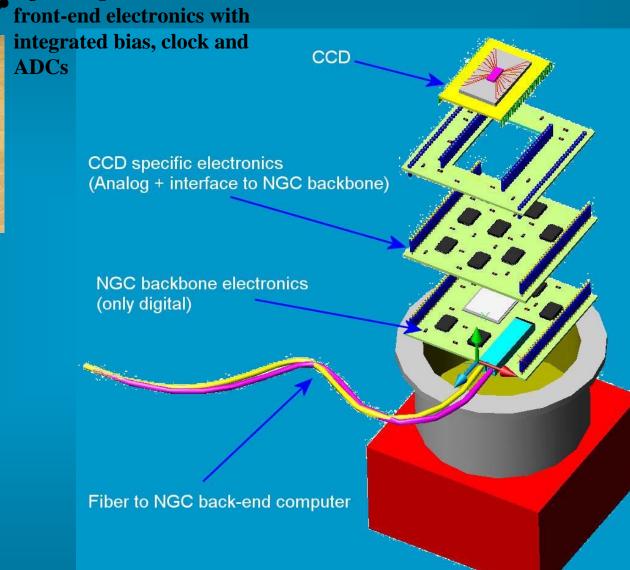
CCD head for AO

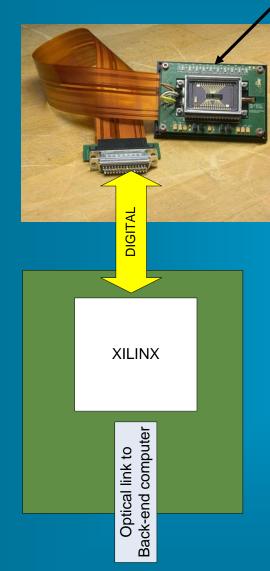




A possible implementation

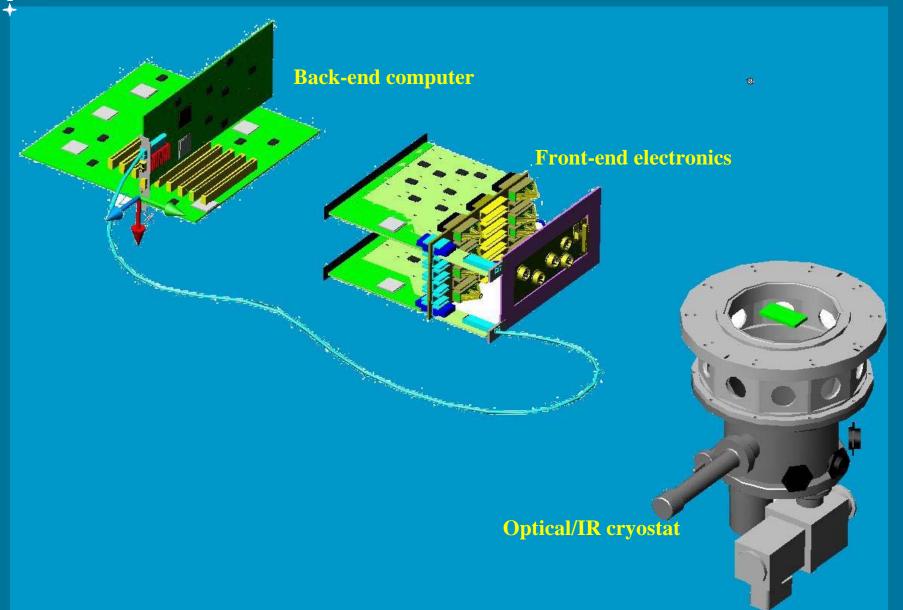
Upcoming AO heads will have front and electronics with



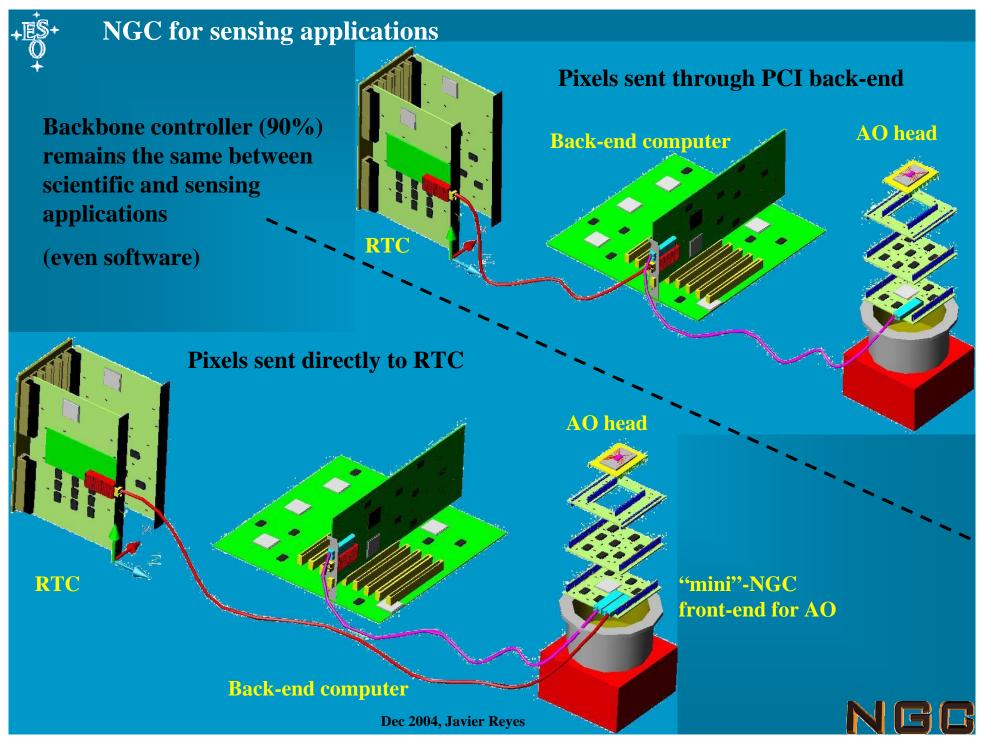




NGC for scientific applications

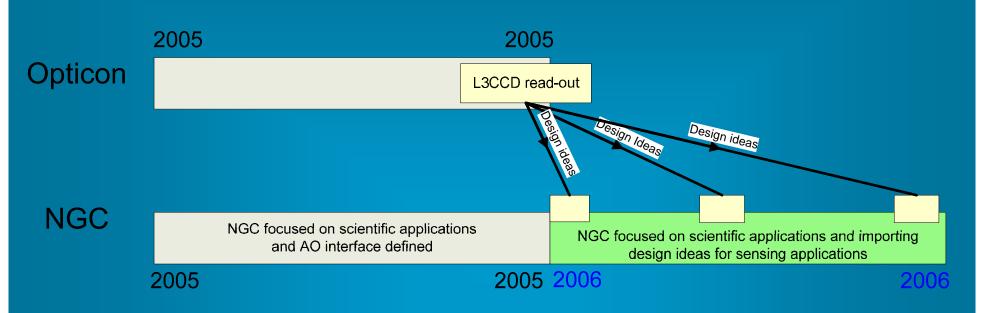








Some thoughts



Definition of an interface could be the key to incorporate design ideas and experience into NGC.

The "mini"-NGC backbone can also address the read-out of ASICs.

