NGC progress, common platform and deliverables

GENERAL

• All forthcoming detectors are easy to address individually
• The complexity is in reaching commonality due to the diversity in requirements
• Conceive a flexible and common platform
• Some technical open questions still to be solved
• We don’t have a united view yet
• We have not agreed yet on whether this is a one-person project or several-person project
NGC common platform for standard systems
NGC common hardware platform

- Standard line card set
- Specially cases covered with add-ons
NGC common hardware platform
3U option

The common platform allows 3U electronics
Deliverables

Video acquisition board

Design subjected to trustful oversampling test
Deliverables

Video acquisition board alternative

- Video acquisition
- Infrared
- 32 channels
- 16-bit @ 3Msps

- 384Mb/s throughput
- Only 8 channels used

- High speed links
- Sequencer
- Control

- CCD support
  (signal conditioning)
  8 channels
Deliverables
Clock/Bias board

The platform supports **add-ons** for special detectors and does not limit future design of **dedicated boards**.
Deliverables
Back-end board

- RTC interface in agreement with AO group
- TIM interface for absolute time triggering
- PULPO interface ???
- PCI-Express
- Decoupling detector specific function from computer bus: Two-chip solution
## Work distribution

**A possible work distribution for the hardware**

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<tr>
<th></th>
<th>Some one else?</th>
<th>Mark</th>
<th>Christoph</th>
<th>Leander</th>
<th>Manfred</th>
<th>Roland</th>
<th>Javier</th>
<th>Jesper</th>
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<td>Bias part of Clock/Bias board</td>
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<td>Clock part of the Clock/Bias board</td>
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<td>Multilevel support on clock board (**)</td>
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